L Number	Hits	Search Text	DB	Time stamp
1	21	FEPS and Sun and microsystems and fast and ethernet and parallel and port	USPAT	2004/07/15 14:04

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L Number	Hits	Search Text	DB	Time stamp
1	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT	2004/07/15 14:24
2	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
3	183	emulation adj interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
4	18	(emulation adj interface) and (network adj connection)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25

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L Number	Hits	Search Text	DB	Time stamp
1	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT	2004/07/15 14:24
2	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
3	183	emulation adj interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
4	18	(emulation adj interface) and (network adj connection)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:36
5	1	(processor adj (emulation or emulator or emulating)) and network and FPGA and TCP	USPAT	2004/07/15 14:37

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L Number	Hits	Search Text	DB	Time stamp
1	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT	2004/07/15 14:24
2	0	((emulation adj interface) same (connected)) and ethernet and MAC and network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
3	183	emulation adj interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:25
4	18	(emulation adj interface) and (network adj connection)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 14:36
5	1	(processor adj (emulation or emulator or emulating)) and network and FPGA and TCP	USPAT	2004/07/15 14:40
6	5	(media adj access adj controller) same (emulation or emulator ro emulating)	USPAT	2004/07/15 14:42
7	2	(media adj access adj controller) and (emulation or emulator ro emulating) and FPGA and ASIC	USPAT	2004/07/15 14:43
8	0	ASIC and development and tool and MAC.ti. and emulat$4	USPAT	2004/07/15 14:44
9	2	ASIC and development and tool and emulator.ti. and emulat$4	USPAT	2004/07/15 15:43
10	0	aptix.as. and quickturn.as. and emulat$4.ti. and MAC.ti.	USPAT	2004/07/15 15:44
11	0	aptix.as. and quickturn.as. and emulat$4.ti.	USPAT	2004/07/15 15:44
12	77	aptix.as. or quickturn.as. and emulat$4.ti.	USPAT	2004/07/15 15:44
13	34	aptix.as. or quickturn.as. and emulat$4.ti. and MAC.ti.	USPAT	2004/07/15 15:45
14	0	(aptix or quickturn).as. and (emulat$4).ti. and (MAC).ti.	USPAT	2004/07/15 15:45
16	0	((aptix or quickturn).as. and (emulat$4).ti.) and (media adj access adj controller)	USPAT	2004/07/15 15:46
17	16	((aptix or quickturn).as. and (emulat$4).ti.) and (MAC)	USPAT	2004/07/15 15:48
18	43	(aptix or quickturn).as. and (emulat$4).ti.	USPAT	2004/07/15 15:48

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